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FILING OR 371 DRAWINGS TOT CLMS IND CLMS ART UNIT FIL FEE REC'D ATTY.DOCKET NO APPL NO. (c) DATE 27 13 3 2812 770 501.38505CX1 10/760,358 01/21/2004

-- 501.38505CX2"

**CONFIRMATION NO. 8710** 

020457 ANTONELLI, TERRY, STOUT & KRAUS, LLP 1300 NORTH SEVENTEENTH STREET **SUITE 1800 ARLINGTON, VA 22209-9889** 

FILING RECEIPT OC000000012460591\*

Date Mailed: 04/27/2004

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

Naoki Yamamoto, Saitama, JAPAN; Yoshikazu Tanabe, Saitama, JAPAN;

Domestic Priority data as claimed by applicant

This application is a CON of 09/577,671 05/25/2000

**Foreign Applications** 

JAPAN 11-158089 06/04/1999

If Required, Foreign Filing License Granted: 04/27/2004

Projected Publication Date: 08/05/2004

Non-Publication Request: No

Early Publication Request: No

Title

Semiconductor integrated circuit device and method for manufacturing the same

438

# LICENSE FOR FOREIGN FILING UNDER Title 35, United States Code, Section 184 Title 37, Code of Federal Regulations, 5.11 & 5.15

#### **GRANTED**

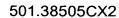
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### IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants:

N. YAMAMOTO, et al

Serial No.:

10/760,358

Filed:

January 21, 2004

Title:

SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE AND

METHOD FOR MANUFACTURING THE SAME

Group:

2812

## REQUEST FOR CORRECTED FILING RECEIPT

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450 April 30, 2003

Sir:

Attached hereto is the Official Filing Receipt for the above-identified application, wherein it is noted that the Attorney Docket Number is incorrect.

Accordingly, it is respectfully requested that a corrected filing receipt be issued indicating that the Attorney Docket Number data to be **--501.38505CX2--** instead of 501.38505CX1.

Respectfully submitted,

ANTONELLI, TERRY, STOUT & KRAUS, LLP

Ronald J. Shore

Registration No.: 28,577

RJS/rr Attachments